

OCT 27 2005

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October 27, 2005

Honorable Commissioner for Patents
Alexandria, VA 22313

Attention: Examiner M. LEWIS
Central Fax - Facsimile No. 571-273-8300

Re: **O. SUZUKI et al - U.S. Appln. Ser. No. 10/722,597**
Attorney Docket No.: 520.43306X00

SUBMISSION of RESPONSE TO RESTRICTION REQUIREMENT

Sir:

Applicant hereby submits the attached paper entitled, "Response to Restriction Requirement (1 page)" in the above-identified application.

CERTIFICATE OF TRANSMISSION:

I hereby certify that the attached papers entitled "RESPONSE TO RESTRICTION REQUIREMENT (1 page)" is being formally filed in the USPTO via Facsimile No. 571-273-8300 on 27 October 2005.


David K. Snyder

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OCT 27 2005

520.43306X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: O. SUZUKI et al
Serial No.: 10/722,597
Filed: November 28, 2003
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
SEMICONDUCTOR INTEGRATED CIRCUIT CHIP THEREOF
Group: 2822
Examiner: M. LEWIS

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
POB 1450
Alexandria, VA 22313-1450

October 27, 2005


Sir:

In reply to the Office Action, dated September 29, 2005, applicants hereby elect the Group I (including claims 1-13 and 15-17), drawn to a semiconductor integrated circuit chip with a heat transfer layer that comprises a flow passage, *without traverse*.

If the Examiner believes that there are any other points which may be clarified or otherwise disposed of either by telephone discussion or by personal interview, the Examiner is invited to contact Applicants' undersigned attorney at the number indicated below.

To the extent necessary, applicants petition for an extension of time under 37 CFR §1.136. Please charge any shortage in the fees due in connection with the filing of this paper, to the Deposit Account of Antonelli, Terry, Stout & Kraus, LLP, Dep. Acct. No. 01-2135 (520.43306X00), and please credit any excess fees to such deposit account.

Respectfully submitted,
ANTONELLI, TERRY, STOUT & KRAUS, LLP



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